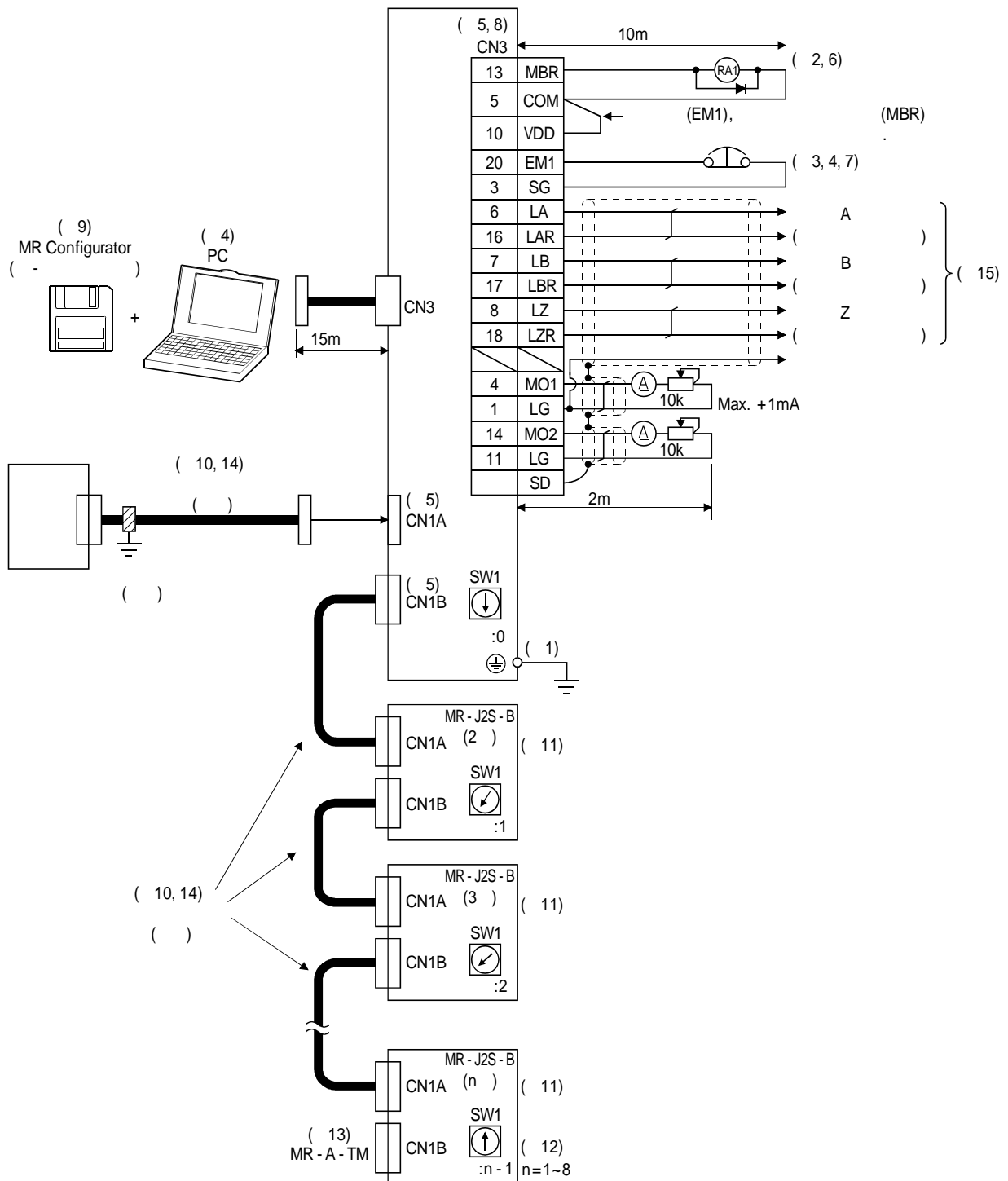


3.1 제어신호계의 접속 예



3.1.1 MR-J2S-700B 이하

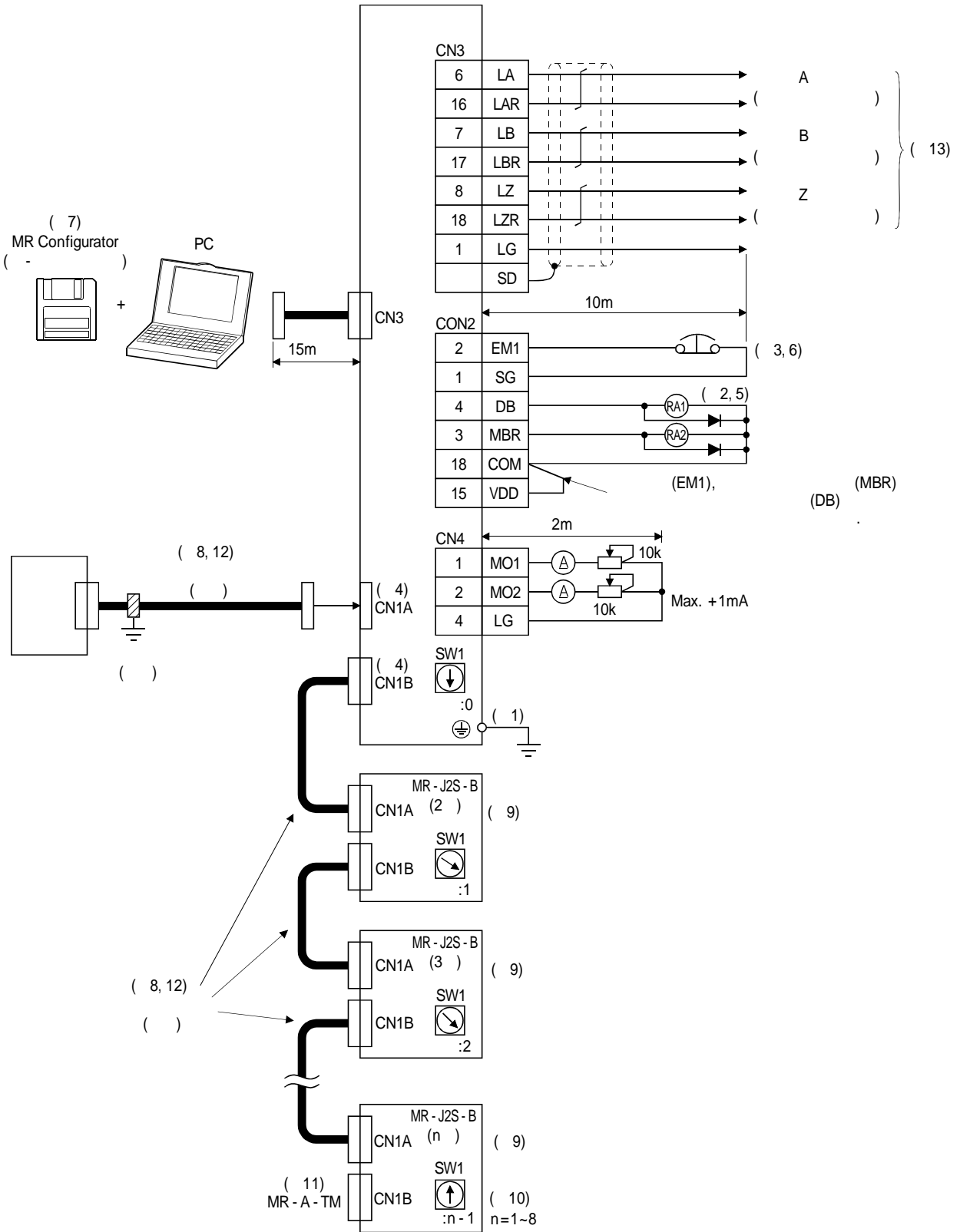


1. () (PE) (⊖ 가) () (PE)
2. 가 가 , (EM1)
3. (B)
4. PC DP , (MR - J2CN3TM) (EM1)
5. CN1A · CN1B · CN2 CN3 (12.1.6)
6. 80mA 가
7. (EM1) ON (B) No.23 “0001” (EM1)
8. (MO1) · (MO2) PC (12.1.3)
9. MRZJW3 - SETUP151
10. 30m (3~4)
11. 2
12. 8 (n=1~8) MR - J2S - B · MR - J2 - 03B5
13. CN1B (MR - A - TM)
14. SSCNET

		MR - J2S - B	MR - J2 - 03B5
QD75M		MR - J2HBUS M	
	Q172CPU(N)	Q172J2BCBL M(-B)	
	Q173CPU(N)	Q173J2B CBL M	
	A	MR - J2HBUS M - A	
MR - J2S - B · MR - J2 - 03B5		MR - J2HBUS M	

15. A1SD75M(AD75M) , 가
A1SD75M(AD75M)

3.1.2 MR-J2S-11KB 이상



1. () (PE) (⊕ 가) () (PE)
2. 가 가 , (EM1)
3. (B)
4. CN1A · CN1B · CN2 CN3
5. 80mA 가
6. (EM1) ON (B) No.23 “0001” (EM1)
7. MRZJW3 - SETUP151
8. 30m
(3~4)
9. 2
10. 8 (n=1~8) MR - J2S - B · MR - J2 - 03B5
11. CN1B (MR - A - TM)
12. SSCNET

		MR - J2S - B	MR - J2 - 03B5
QD75M		MR - J2HBUS M	
	Q172CPU(N)	Q172J2BCBL M(- B)	
	Q173CPU(N)	Q173J2B CBL M	
	A	MR - J2HBUS M - A	
MR - J2S - B · MR - J2 - 03B5		MR - J2HBUS M	

13. A1SD75M(AD75M) , 가
A1SD75M(AD75M)